

**SUBMISSION OF PROPOSED DRAWING AMENDMENT FOR
APPROVAL BY EXAMINER (37 CFR 1.123)**

Docket No.
RA-133

In Re Application Of: **SIDIROPOULOS et al.**

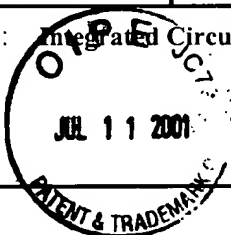
Serial No.
09/372,879

Filing Date
August 12, 1999

Examiner
Tran, T

Art Unit
2814

Invention: **Integrated Circuit Device Having I/O Structures with Reduced Input Loss**



Address to:
Assistant Commissioner for Patents
Washington, D.C. 20231

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TECHNOLOGY CENTER 2800

Attached please find:

(check applicable items)

- ☐ a sketch in permanent ink
- ☒ a copy of the original drawing(s) with red ink

showing proposed changes to the drawing(s) in this application for which the approval of the examiner is requested.

Stephen R. Whitt
Signature

Dated: July 11, 2001

I certify that this document and fee is being deposited
on JUL 11, 2001 with the U.S. Postal Service as
first class mail under 37 C.F.R. 1.8 and is addressed to the
Assistant Commissioner for Patents, Washington, D.C.
20231.

Stephen R. Whitt

Signature of Person Mailing Correspondence

Stephen R. Whitt

Typed or Printed Name of Person Mailing Correspondence

100

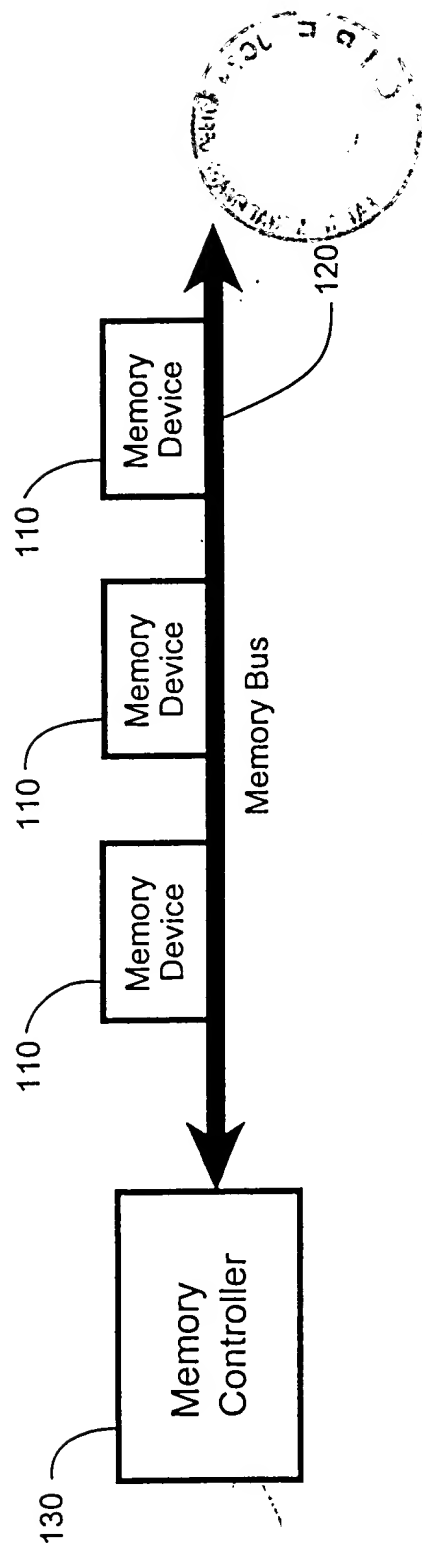


FIGURE 1
(PRIOR ART)

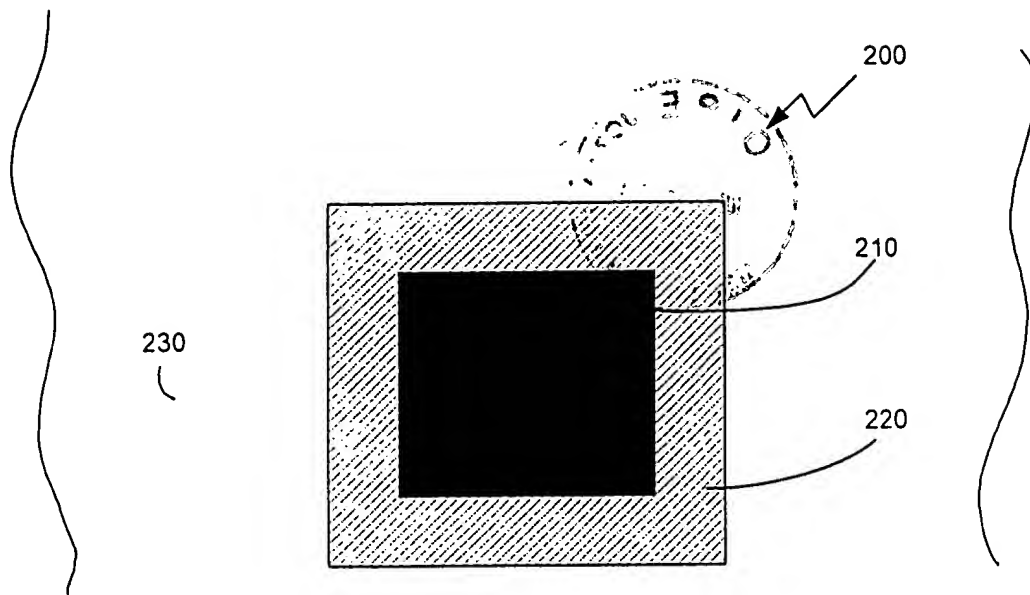


FIGURE 2A
(PRIOR ART)

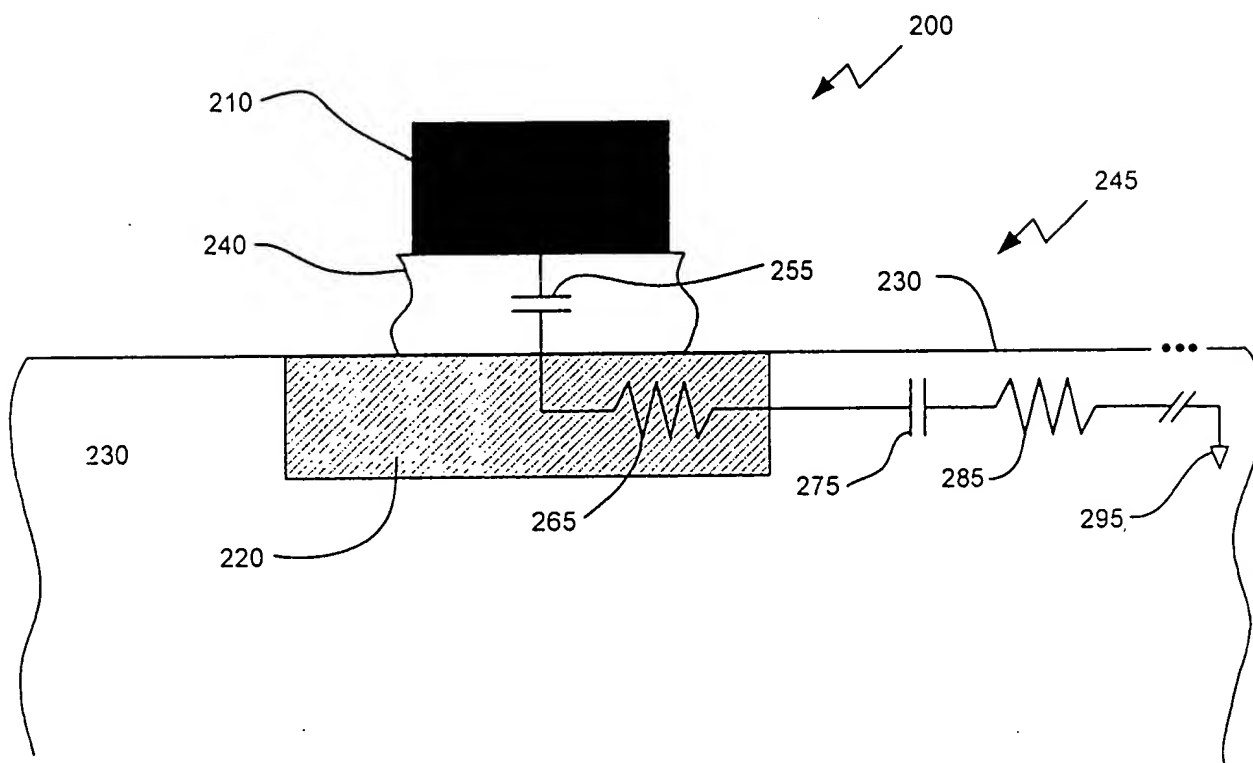


FIGURE 2B
(PRIOR ART)

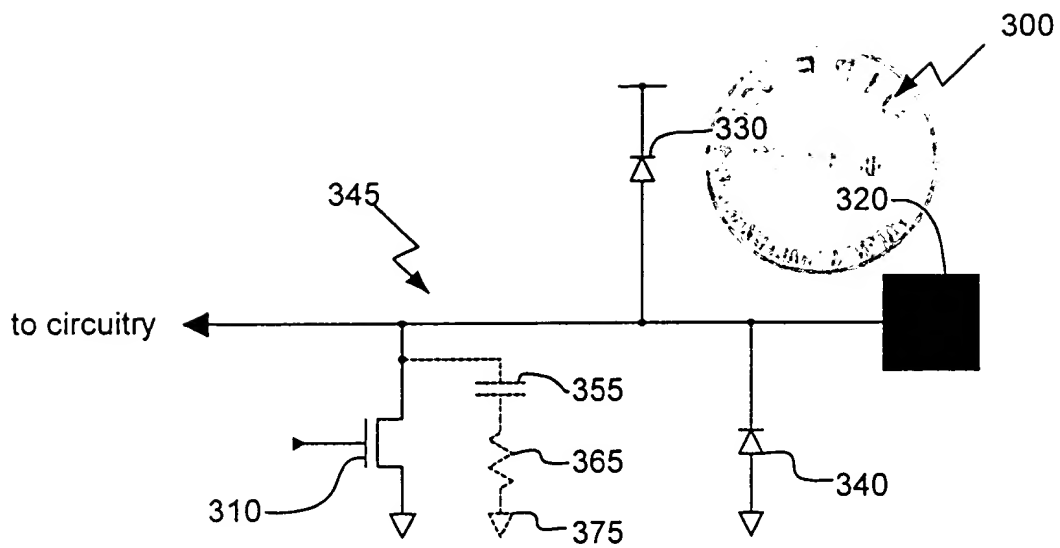


FIGURE 3A
(PRIOR ART)

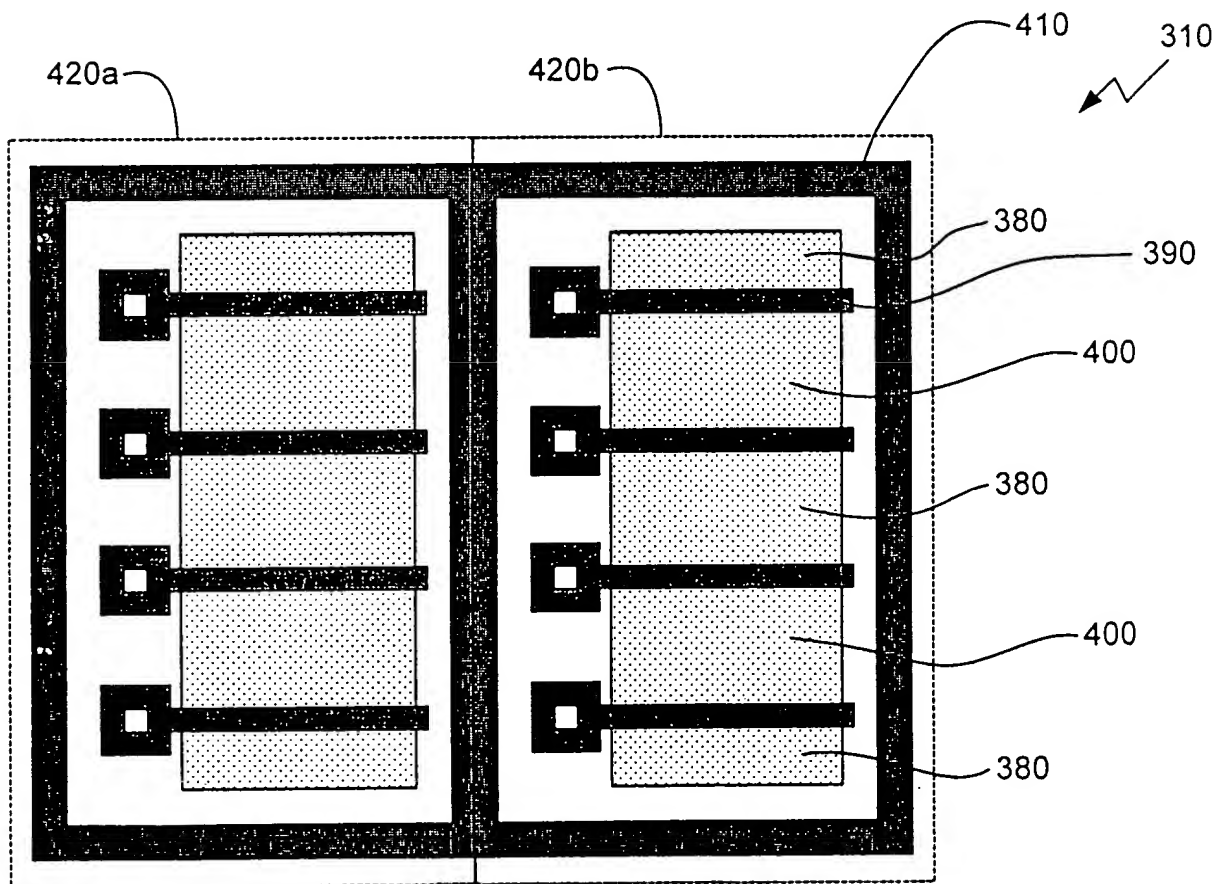


FIGURE 3B
(PRIOR ART)